







• A multirate structural interpretation of the polyphase decomposition is given below







Polyphase Decomposition

- The polyphase decomposition of an IIR transfer function H(z) = P(z)/D(z) is not that straight forward
- One way to arrive at an *M*-branch polyphase decomposition of H(z) is to express it in the form $P'(z)/D'(z^M)$ by multiplying P(z) and D(z) with an appropriately chosen polynomial and then apply an *M*-branch polyphase decomposition to P'(z)

7

















• A direct realization of *H*(*z*) based on the Type II polyphase decomposition is shown below







Computationally Efficient Decimators

- To illustrate the computational efficiency of the modified decimator structure, assume H(z) to be a length-N structure and the input sampling period to be T = 1
- Now the decimator output *y*[*n*] in the original structure is obtained by downsampling the filter output *v*[*n*] by a factor of М

Copyright © 2010, S. K. Mith

Copyright © 2010, S. K. Mitra

20

22

Computationally Efficient Decimators • It is thus necessary to compute v[n] at $n = \dots, -2M, -M, 0, M, 2M, \dots$

- Computational requirements are therefore N multiplications and (N-1) additions per output sample being computed
- However, as *n* increases, stored signals in the delay registers change

21

Computationally Efficient Decimators

- Hence, all computations need to be completed in one sampling period, and for the following (M-1) sampling periods the arithmetic units remain idle
- The modified decimator structure also requires N multiplications and (N-1)additions per output sample being computed

Computationally Efficient Decimators and Interpolators

- However, here the arithmetic units are operative at all instants of the output sampling period which is 1/M times that of the input sampling period
- of the interpolator structure developed using the polyphase decomposition

23

• Similar savings are also obtained in the case

Copyright © 2010, S. K. Mitra

Copyright © 2010, S. K. Mitra

Computationally Efficient Interpolators



Computationally Efficient Decimators and Interpolators

- More efficient interpolator and decimator structures can be realized by exploiting the symmetry of filter coefficients in the case of linear-phase filters H(z)
- Consider for example the realization of a factor-of-3 (M = 3) decimator using a length-12 Type 1 linear-phase FIR lowpass filter

25

Copyright © 2010, S. K. Mitr



Copyright © 2010, S. K. Mitr

Computationally Efficient Decimators and Interpolators

- Note that $E_1(z)$ still has a symmetric impulse response, whereas $E_0(z)$ is the mirror image of $E_2(z)$
- These relations can be made use of in developing a computationally efficient realization using only 6 multipliers and 11 two-input adders as shown on the next slide

27

29

Copyright © 2010, S. K. Mitr

Computationally Efficient Decimators and Interpolators

• Factor-of-3 decimator with a linear-phase decimation filter



Rational Sampling Rate Converter

- The complexity of the design of the fractional sampling rate converter depends on the ratio of the sampling rates between the input and the output digital signals
- For example, in digital audio applications, the three different sampling frequencies employed are 44.1 kHz, 32 kHz, and 48 kHz

Copyright © 2010, S. K. Mitra

Rational Sampling Rate Converter

- As a consequence there are three different values for the sampling rate conversion factor:
- 2:3 (or 3:2), 147:160 (or 160:147), and 320:441 (or 441:320)
- Likewise, in digital video applications, the sampling rates of composite video signals are 14.3181818 MHz and 17.734475 MHz

30

Rational Sampling Rate Converter

- The sampling rates of the digital component video signal are 13.5 MHz and 6.75 MHz for the luminance and the color-difference signals, respectively, for the NTSC and PAL systems
- Here, again there are different sampling rate conversion factors

31

Copyright © 2010, S. K. Mitra

Rational Sampling Rate Converter

- We outline next the implementation of a computationally efficient FIR fractional rate converter with a rational conversion factor *L/M*, where *L* and *M* are mutually prime
- Now two mutually prime integers *L* and *M* satisfy the relation

 $\mu M - \lambda L = 1$

where μ and λ are unique distinct positive integers

Copyright © 2010, S. K. Mitra



32













Rational Sampling Rate Converter

- For this design we have L = 2 and M = 3
- The identity $3\mu 2\lambda = 1$ is thus satisfied with $\mu = \lambda = 1$
- Hence, the general structure of this converter is as indicated below

43



Copyright © 2010, S. K. Mitra





