Courtesy of CRC Press/Taylor & Francis Group

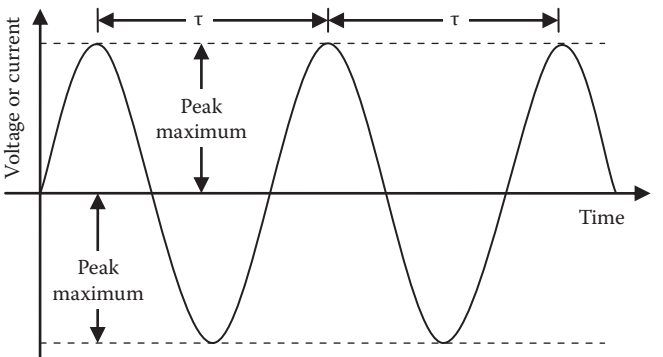

Figure 13.1 Ideal ac waveform.

Courtesy of CRC Press/Taylor & Francis Group

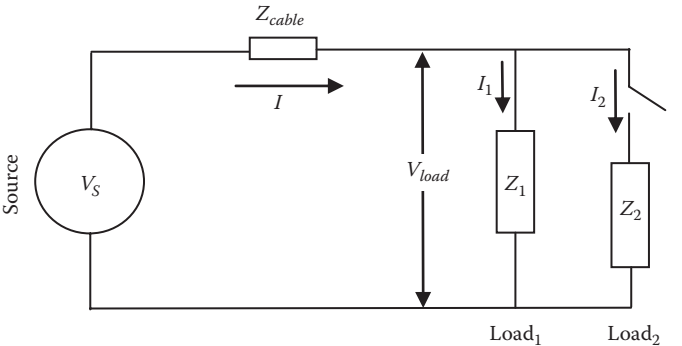

Figure 13.2 Loads connected to voltage source through cable.

013x002.eps

Courtesy of CRC Press/Taylor & Francis Group

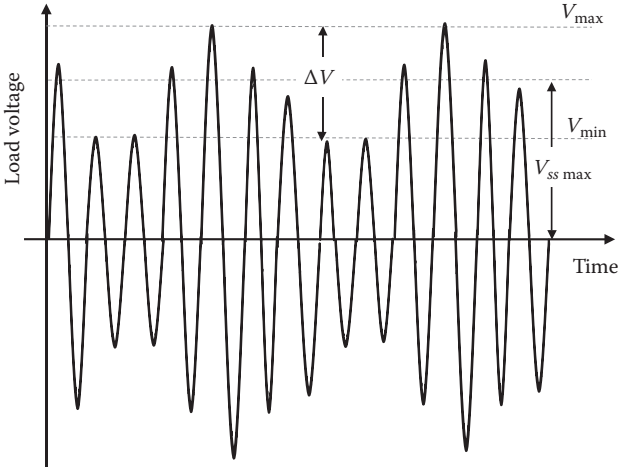

Figure 13.3 Cyclic flicker.

013x003.eps

Courtesy of CRC Press/Taylor & Francis Group

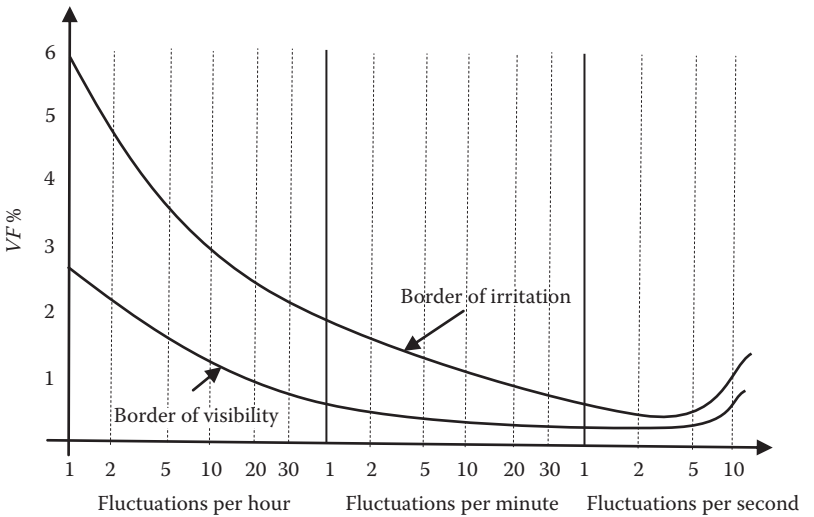

Figure 13.4 Effect of flickers on human.

013x004.eps

Courtesy of CRC Press/Taylor & Francis Group

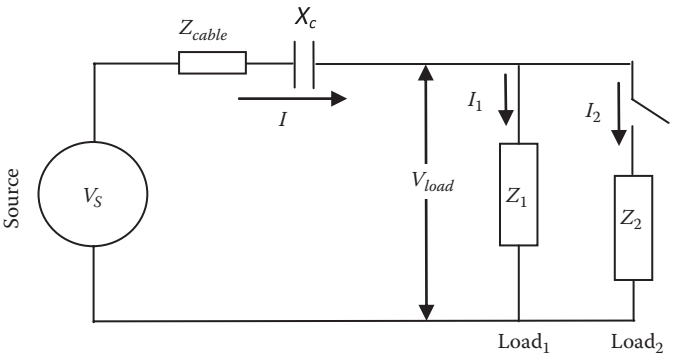

Figure 13.5 Loads connected to source by capacitor compensated cable.

Courtesy of CRC Press/Taylor & Francis Group

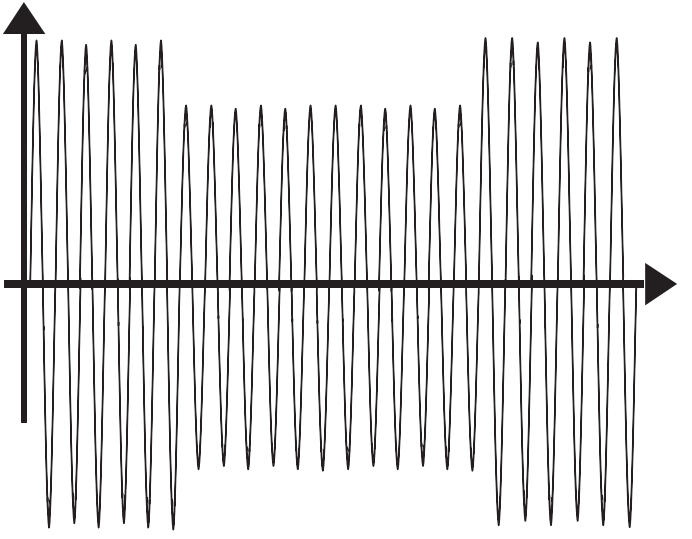

Figure 13.6 Voltage sag (dip).

013x006.eps

Courtesy of CRC Press/Taylor & Francis Group

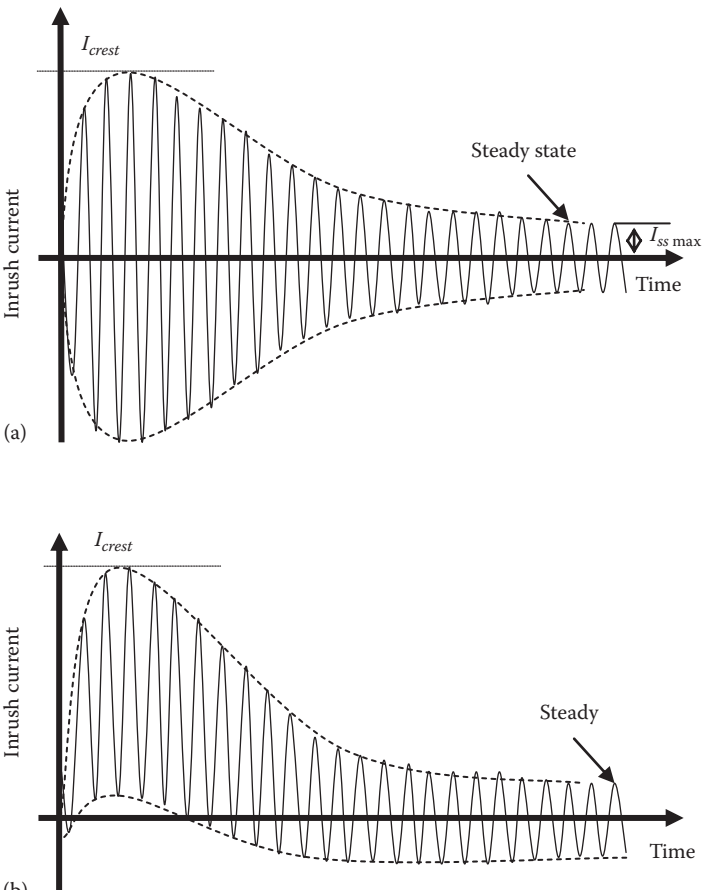

Figure 13.7 Typical inrush current waveforms at motor starting or transformer switching: (a) symmetrical inrush current and (b) asymmetrical inrush current.

013x007.eps

Courtesy of CRC Press/Taylor & Francis Group

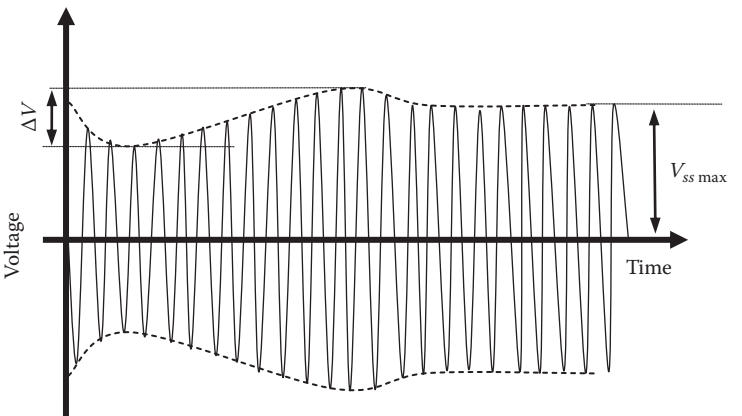

Figure 13.8 Flicker voltage due to symmetrical inrush current.

013x008.eps

Courtesy of CRC Press/Taylor & Francis Group

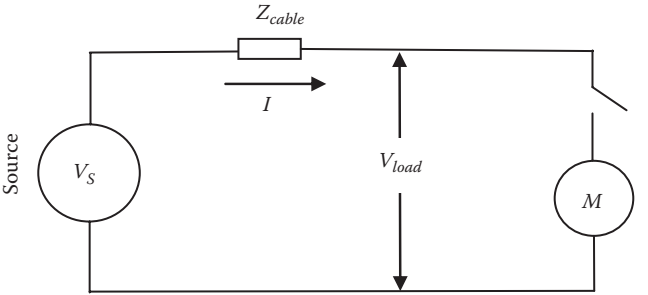

Figure 13.9 Motor connected to source via cable.

013x009.eps

Courtesy of CRC Press/Taylor & Francis Group

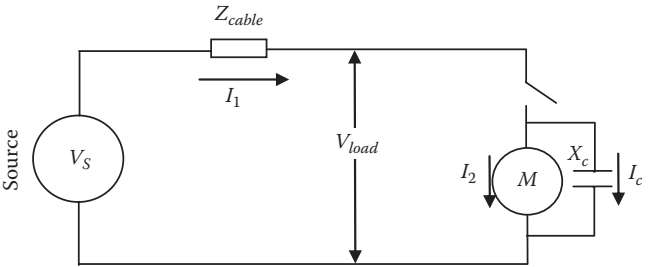

Figure 13.10 Motor with starting capacitor.

013x010.eps

Courtesy of CRC Press/Taylor & Francis Group

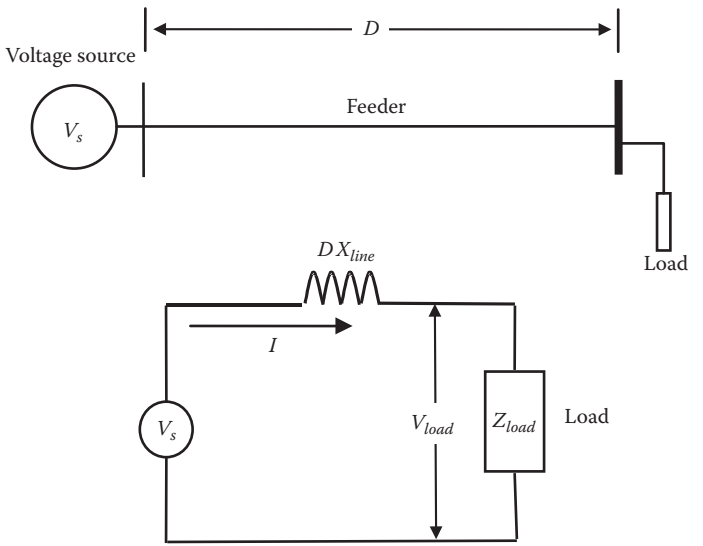

Figure 13.11 A representation of a simple power system.

013x011.eps

Courtesy of CRC Press/Taylor & Francis Group

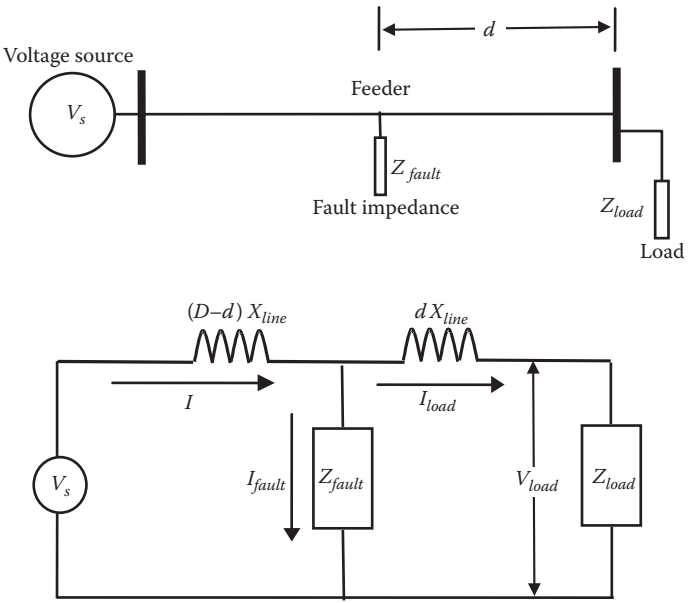

Figure 13.12 A representation of a simple power system with fault between source and load.

Courtesy of CRC Press/Taylor & Francis Group

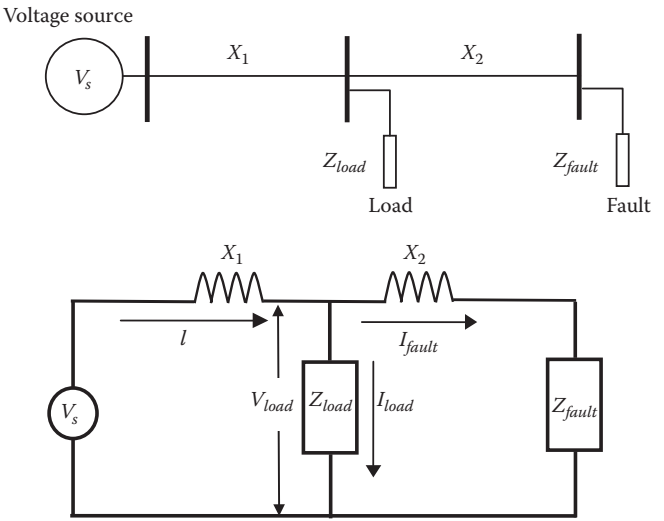

Figure 13.13 A representation of a simple power system with load between the fault and the source.

Courtesy of CRC Press/Taylor & Francis Group

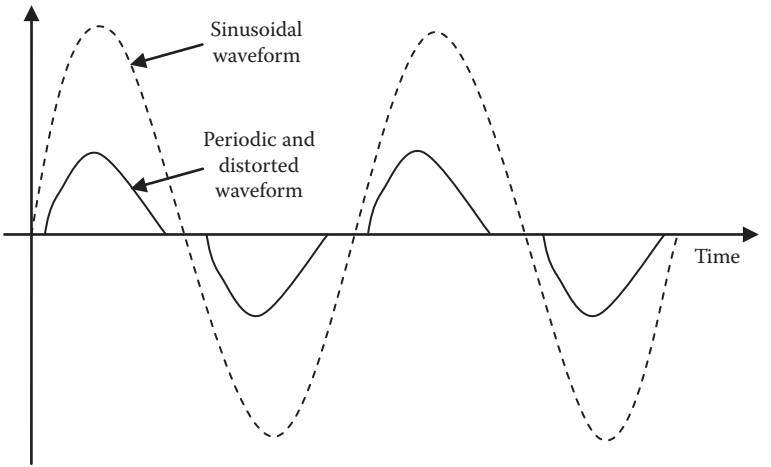

Figure 13.14 Sinusoidal and distorted waveforms.

013x014.eps

Courtesy of CRC Press/Taylor & Francis Group

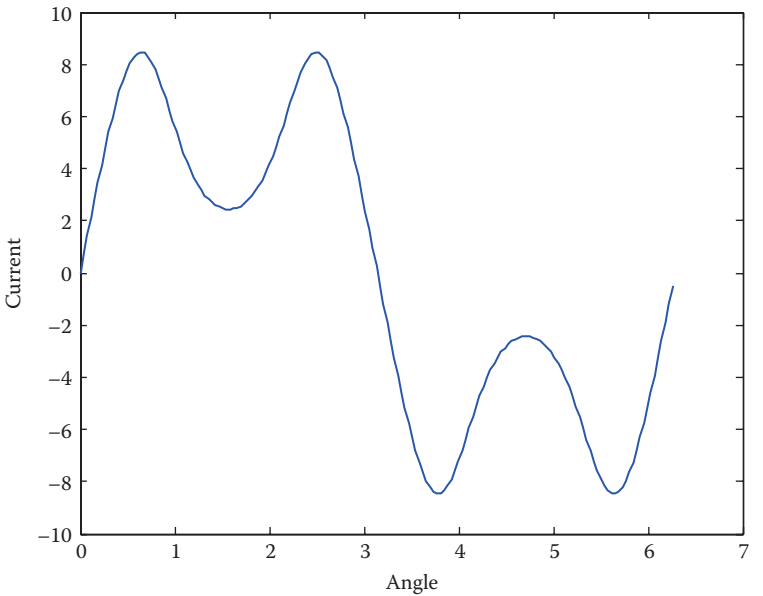

Figure 13.15 Current waveform of a nonlinear resistance.

013x015.eps

Courtesy of CRC Press/Taylor & Francis Group

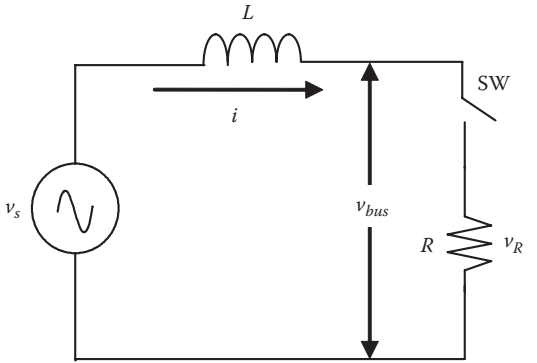

Figure 13.16 A representation of a simple system consists of source, cable, and switching load.

Courtesy of CRC Press/Taylor & Francis Group

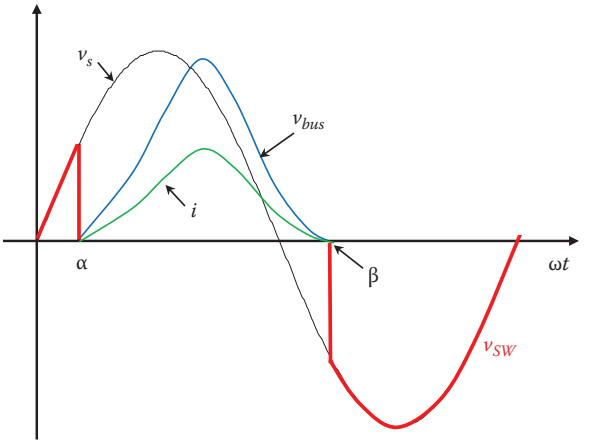

Figure 13.17 Waveforms of the circuit in Figure 13.16.

013x017.eps

Courtesy of CRC Press/Taylor & Francis Group

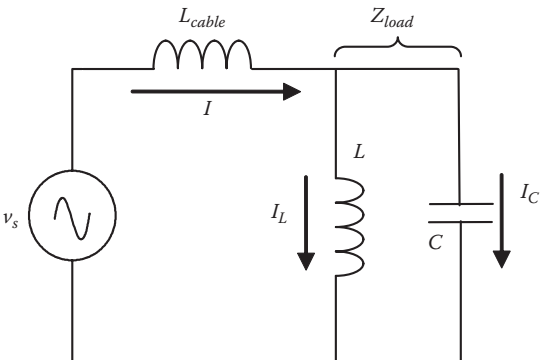

Figure 13.18 Compensated load.