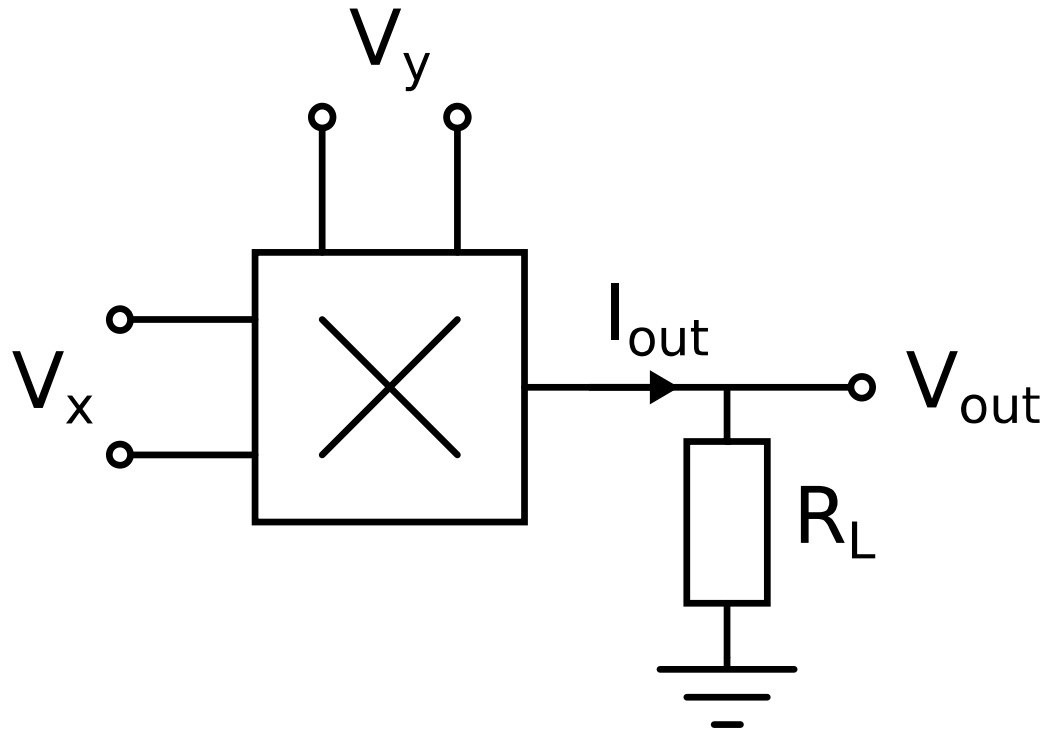


ELEC-E3510 Basics of IC Design

Lecture 9: Multipliers

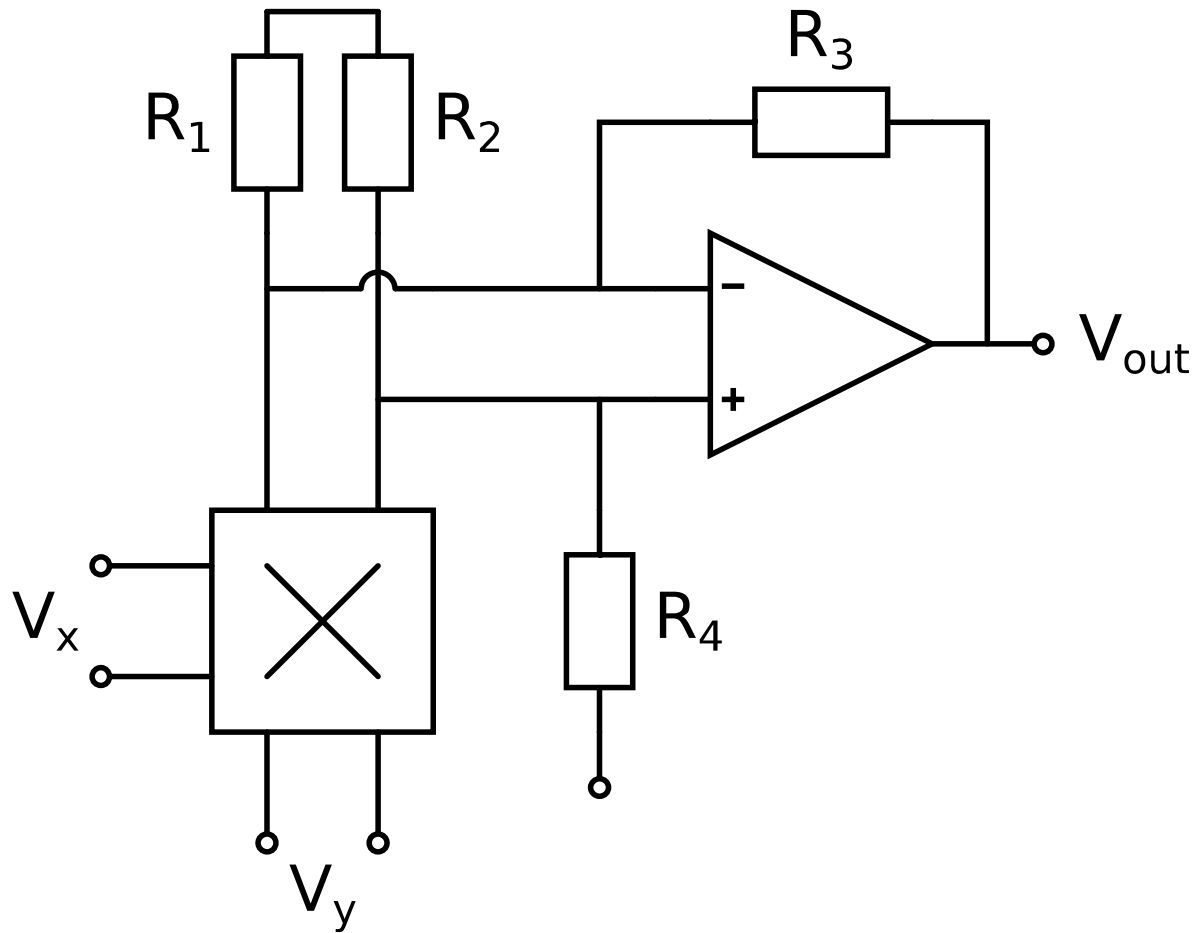
Analog multiplier



$$I_{out} = kV_x V_y$$

$$V_{out} = I_{out} \cdot R_L = kR_L V_x V_y$$

Single-ended output

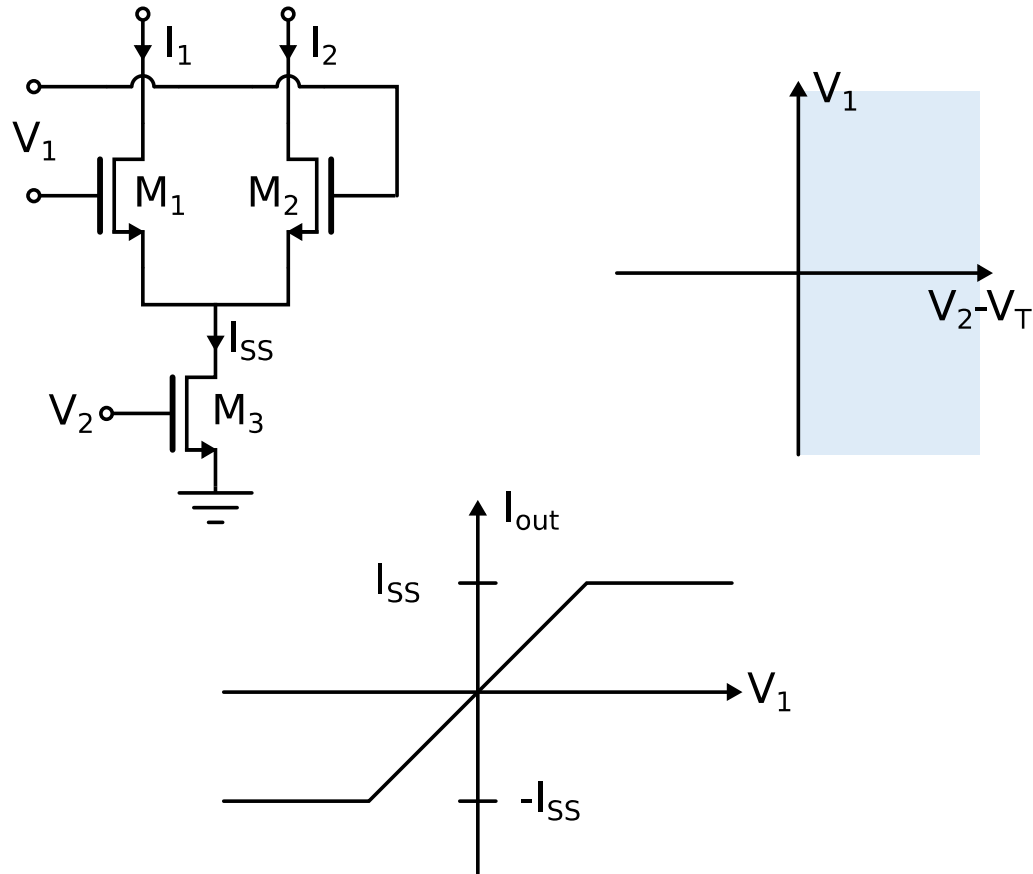


Assume $R_3 = R_4 = R \Rightarrow$

$$V_{out} = R I_{out} + V_{REF}$$

$$V_{out} = \sqrt{2} k R_L V_x V_y + V_{REF}$$

2-quadrant multiplier



For source coupled pair

$$I_{out} = I_1 - I_2 = k_1 V_1 \sqrt{\frac{2I_{SS}}{k_1} - V_1^2}$$

Assume $V_1^2 \ll \frac{2I_{SS}}{k}$; $k_1 = \frac{1}{2} \mu C_{ox}$ $V_i \ll \sqrt{\frac{I_{SS}}{k}}$

$$\begin{aligned} I_{out} &= I_1 - I_2 = g_{m1} V_1 \\ &= k_1 V_1 \sqrt{\frac{2I_{SS}}{k_1}} \end{aligned}$$

M_3 in saturation

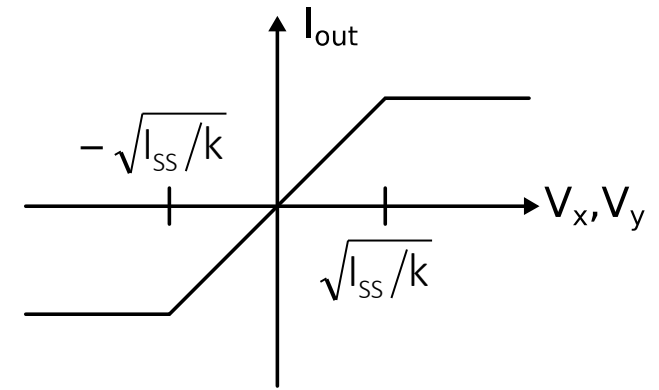
$$\begin{aligned} I_{SS} &= k_3 (V_2 - V_T)^2 \\ \Rightarrow I_{out} &= \sqrt{2k_1} V_1 \sqrt{k_3} (V_2 - V_T) \\ &= \sqrt{2k_1 k_3} (V_1 V_2 - V_1 V_T) \\ &= \sqrt{2k_1 k_3} (V_2 - V_T) \cdot V_1 \end{aligned}$$

Also M_1 and M_2 form a source coupled pair:

$$I_1 = \frac{k}{2} \left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} \right)^2$$

$$I_2 = \frac{k}{2} \left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} - \frac{V_y}{\sqrt{2}} \right)^2$$

$$\Rightarrow I_{out} = kV_x \left[\sqrt{\left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} \right)^2 - V_x^2} - \sqrt{\left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} - \frac{V_y}{\sqrt{2}} \right)^2 - V_x^2} \right]$$



With small signal amplitudes i.e.

$$V_x, V_y \ll \sqrt{\frac{I_{SS}}{k}}$$

$$\Rightarrow I_{out} \approx kV_x \left[\sqrt{\left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} \right)^2 - V_x^2} - \sqrt{\left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} - \frac{V_y}{\sqrt{2}} \right)^2 - V_x^2} \right]$$

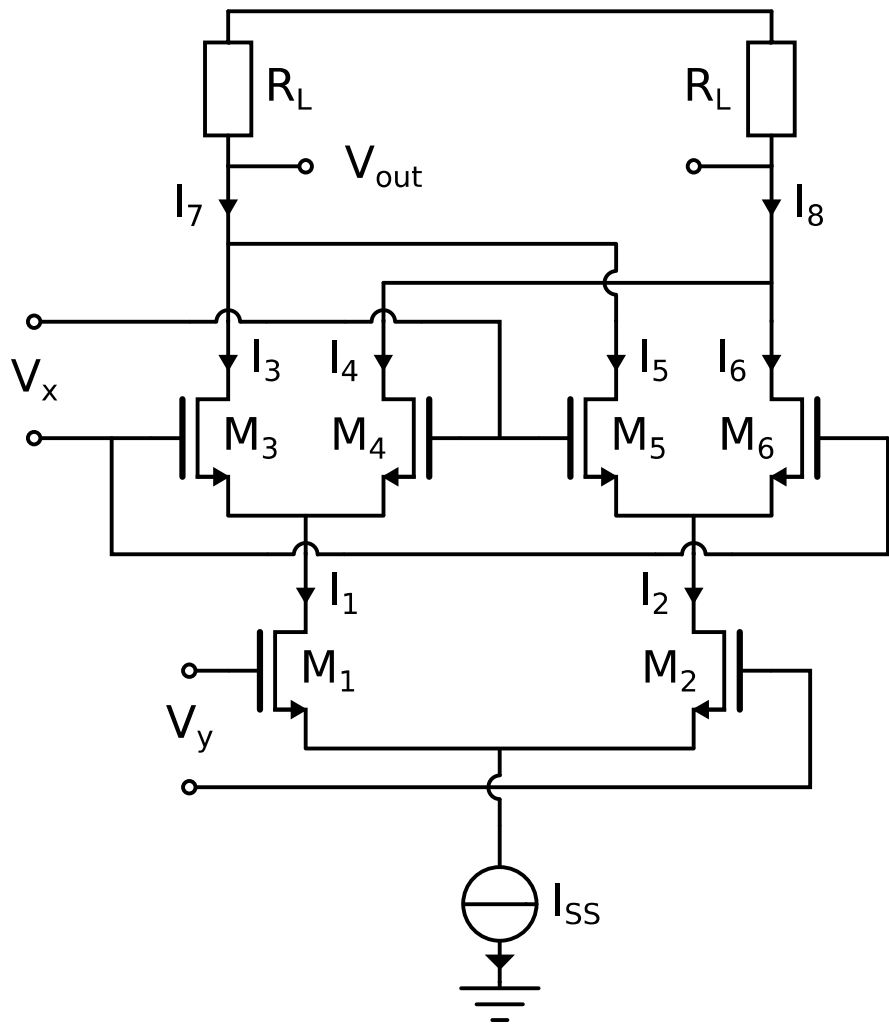
$$\approx kV_x \left[\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} - \sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} \right]$$

$$\Rightarrow I_{out} \approx \sqrt{2}kV_xV_y$$

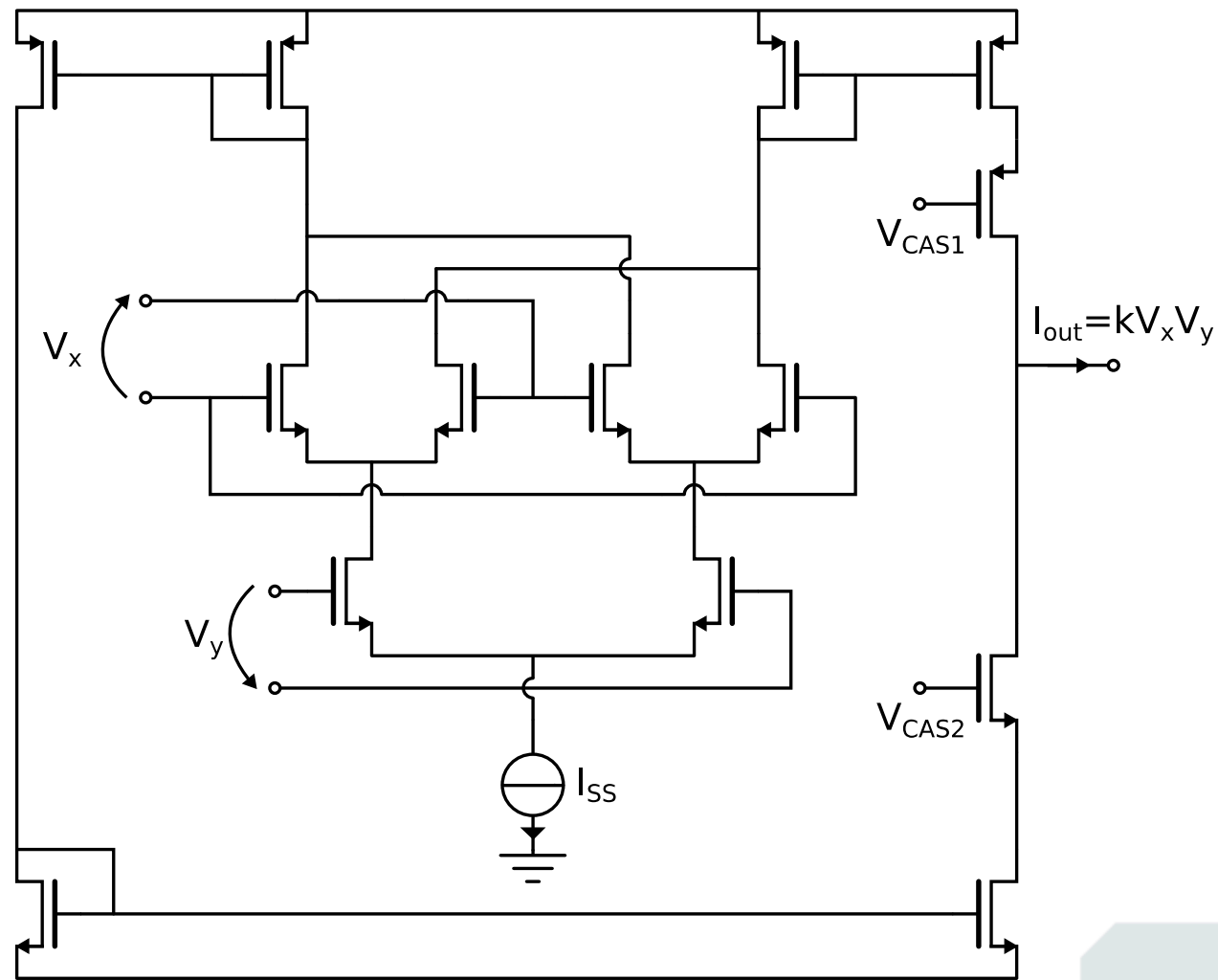
Linear region can be extended by:

1. Increasing I_{SS}
2. Decreasing k (big L , small W)
3. Use small input signal V_i $V_i \ll \sqrt{\frac{I_{SS}}{k}}$
(attenuate input signal)
 \Rightarrow (source coupled pair) $\Delta I \approx V_i \sqrt{2kI_{SS}}$
 \Rightarrow 10 V supply voltage needed!

Differential to single-ended conversion



- Symmetrical load for Gilbert cell



Linearized source coupled pair

Source coupled pair:

$$I_1 = \frac{k}{2} \left(\sqrt{\frac{I_{SS}}{k} - \frac{V_1^2}{2}} + \frac{V_1}{\sqrt{2}} \right)^2$$

$$I_2 = \frac{k}{2} \left(\sqrt{\frac{I_{SS}}{k} - \frac{V_1^2}{2}} - \frac{V_1}{\sqrt{2}} \right)^2$$

Assume: $I_{SS}' = I_{SS} + \frac{1}{2}kV_i^2$

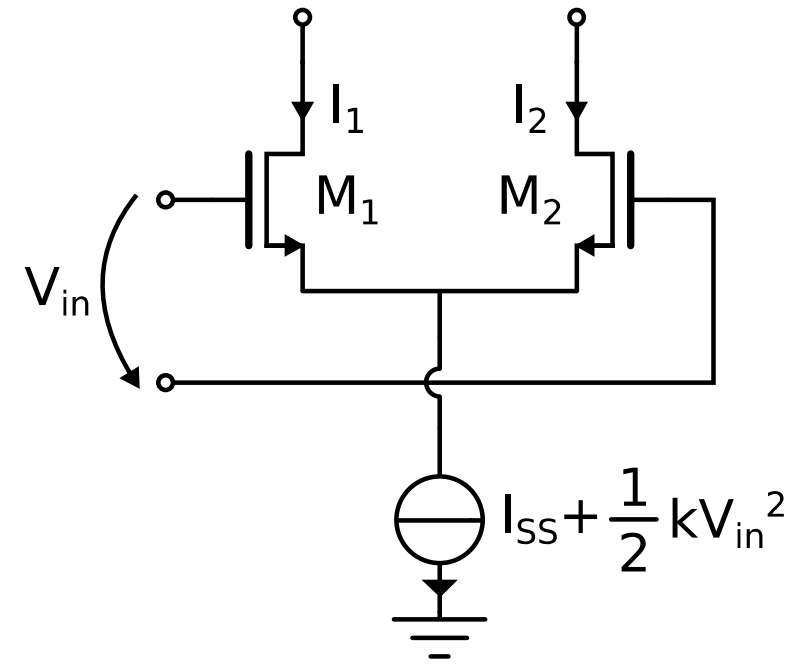
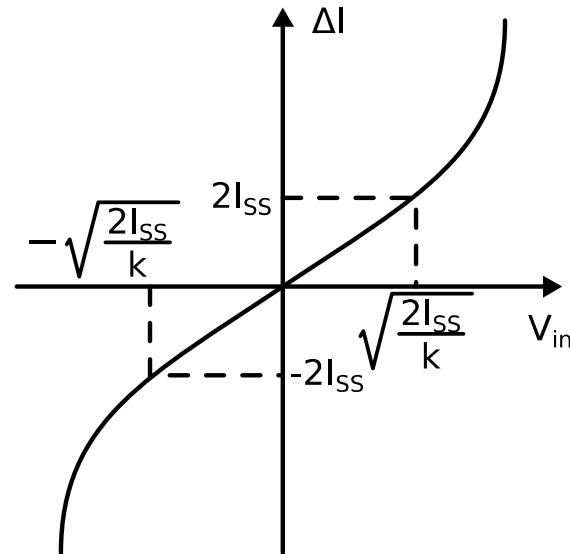
$$\Rightarrow \begin{cases} I_1 = \frac{k}{2} \left(\sqrt{\frac{I_{SS}}{k} + \frac{V_i}{\sqrt{2}}} \right)^2 \\ I_1 = \frac{k}{2} \left(\sqrt{\frac{I_{SS}}{k} - \frac{V_i}{\sqrt{2}}} \right)^2 \end{cases}$$

Thus output current is

$$I_{OUT} = I_1 - I_2 = \frac{k}{2} \left[\left(\sqrt{\frac{I_{SS}}{k} + \frac{V_i}{\sqrt{2}}} \right)^2 - \left(\sqrt{\frac{I_{SS}}{k} - \frac{V_i}{\sqrt{2}}} \right)^2 \right]$$

Which gives

$$I_{OUT} = V_i \sqrt{2kI_{SS}} \quad (\text{exactly!}) \quad \text{when } V_x, V_y \leq \sqrt{\frac{I_{SS}}{k}}$$



Calculate I_{out} :

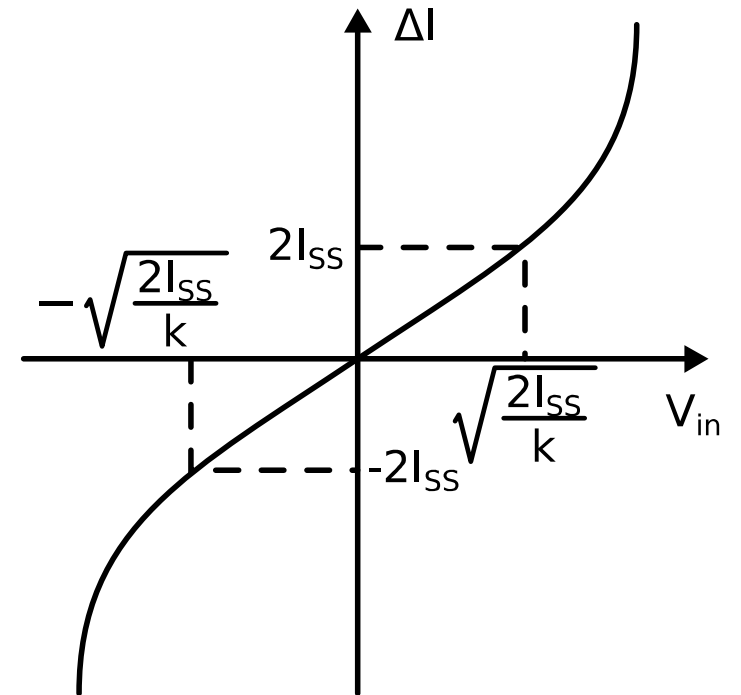
$$I_{OUT} = kV_x \left[\sqrt{\left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} \right)^2} - \sqrt{\left(\sqrt{\frac{I_{SS}}{k} - \frac{V_y^2}{2}} - \frac{V_y}{\sqrt{2}} \right)^2} \right]$$

$$I_{OUT} = \sqrt{2}kV_xV_y \quad (\text{exactly}) \quad \text{when} \quad V_x, V_y \leq \sqrt{\frac{I_{SS}}{k}}$$

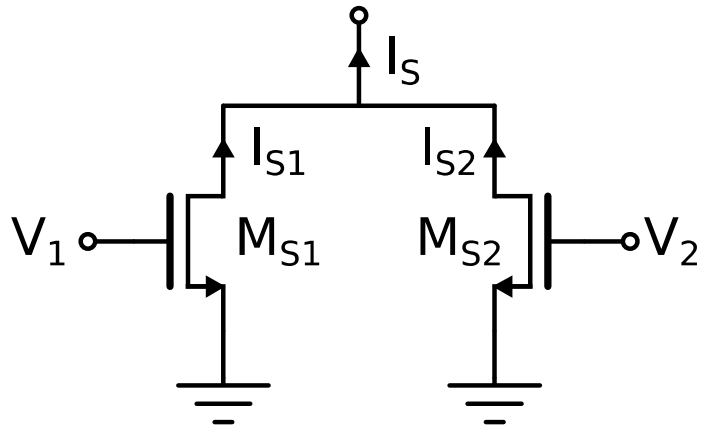
It is adequate to linearize x-input only.
Also y-input becomes linearized.

However, $V_x, V_y \leq \sqrt{\frac{I_{SS}}{k}}$ therefore

⇒ To maximize the linear region, maximize $V_{D1,SAT}$.



Realization of current source $I_{SS} + I_o$



Input signal:

$$V_i = V_{CX} + V_x = V_{DC} + V_{AC}$$

Differential:

$$V_x = V_1 - V_2$$

Common-mode:

$$V_{CX} = \frac{1}{2}(V_1 + V_2)$$

$$\Rightarrow V_1 = V_{CX} + \frac{V_x}{2}$$

$$V_2 = V_{CX} - \frac{V_x}{2}$$

Current equation:

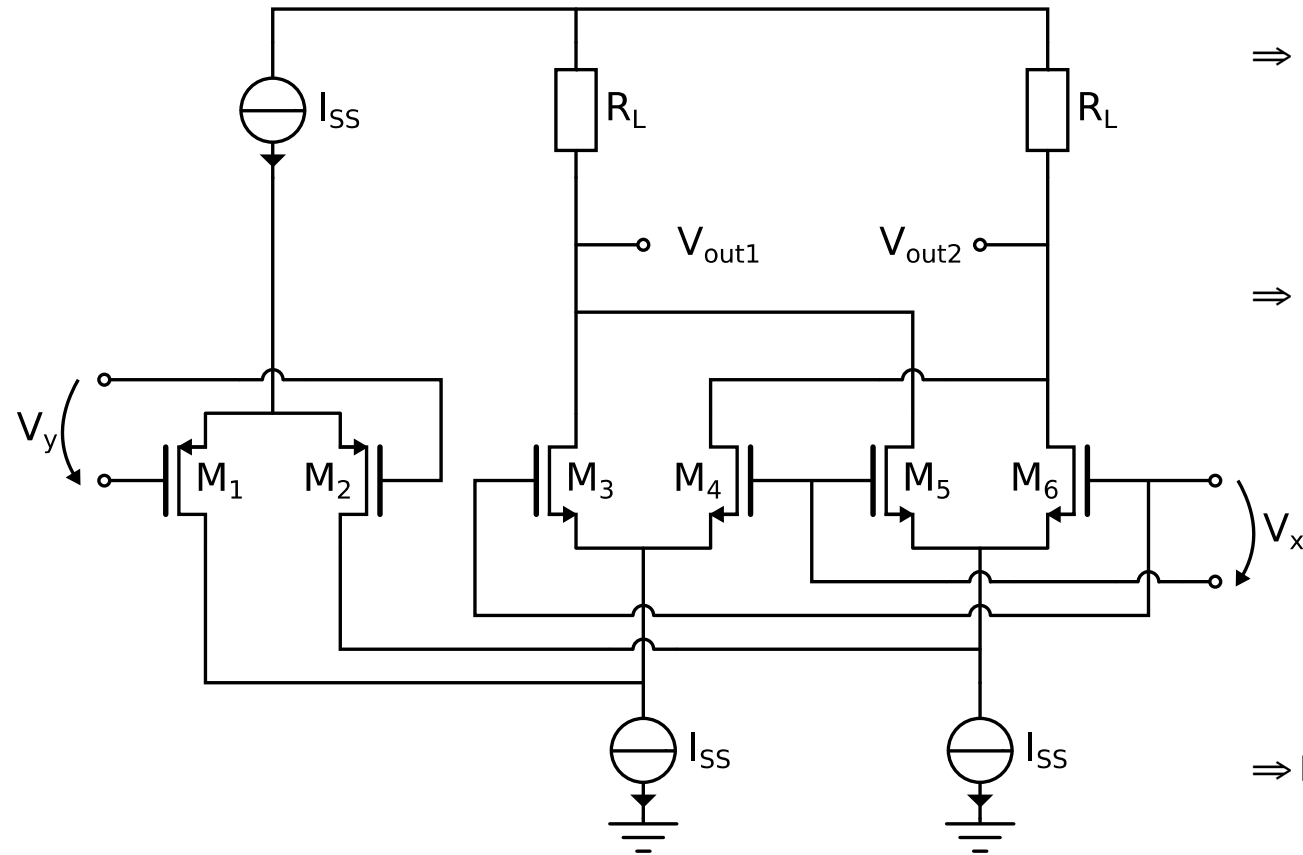
- Assume transistors in saturation

$$I_S = I_{S1} + I_{S2}$$

$$= k_n \left(V_{CX} + \frac{V_x}{2} - V_T \right)^2 + k_n \left(V_{CX} - \frac{V_x}{2} - V_T \right)^2$$

$$= I_{SS} + I_o$$

Folded Gilbert cell



$$I_3 + I_4 = I_{SS} - I_1 = I_2 \quad ; I_1 + I_2 = I_{SS}$$

$$I_6 + I_5 = I_{SS} - I_2 = I_1$$

$$\Rightarrow I_3 - I_4 = k_n V_x \sqrt{\frac{2I_2}{k_n} - V_x^2}$$

$$I_6 - I_5 = k_n V_x \sqrt{\frac{2I_1}{k_n} - V_x^2}$$

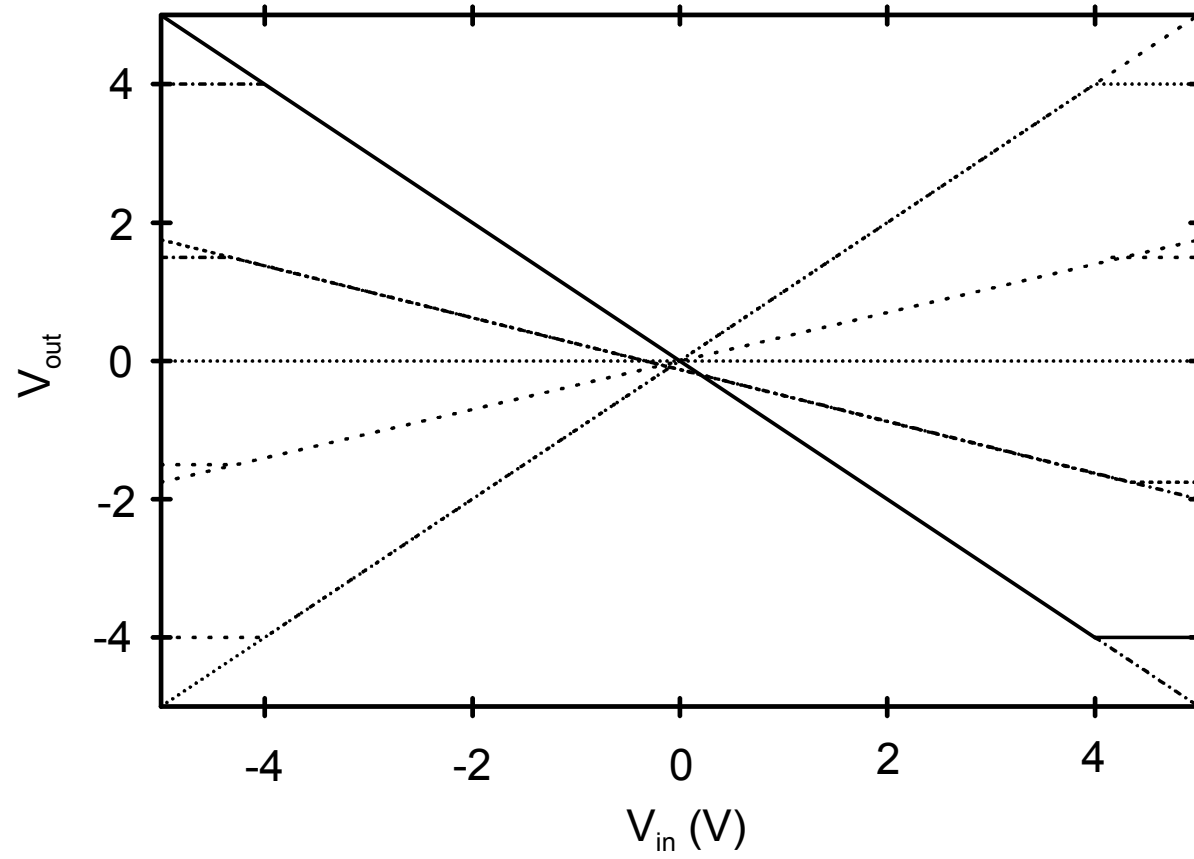
$$\Rightarrow I_{OUT} = k_n V_x \left[\sqrt{\frac{2I_2}{k_n} - V_x^2} - \frac{2I_1}{k_n} - V_x^2 \right]$$

$$I_1 = \frac{k_p}{2} \left(\sqrt{\frac{I_{SS}}{k_p} - \frac{V_y^2}{2}} - \frac{V_y}{\sqrt{2}} \right)^2$$

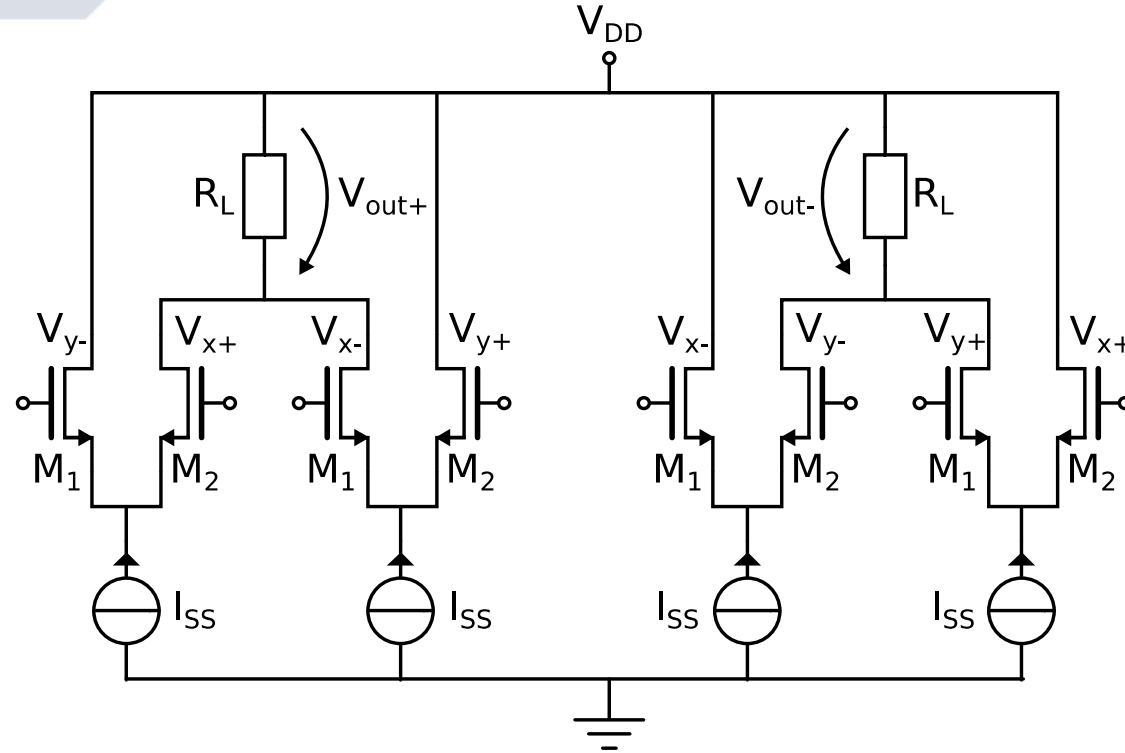
$$I_2 = \frac{k_p}{2} \left(\sqrt{\frac{I_{SS}}{k_p} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} \right)^2$$

$$\Rightarrow I_{OUT} = k_n V_x \left[\sqrt{\frac{k_p}{k_n} \left(\sqrt{\frac{I_{SS}}{k_p} - \frac{V_y^2}{2}} + \frac{V_y}{\sqrt{2}} \right)^2 - V_x^2} - \sqrt{\frac{k_p}{k_n} \left(\sqrt{\frac{I_{SS}}{k_p} - \frac{V_y^2}{2}} - \frac{V_y}{\sqrt{2}} \right)^2 - V_x^2} \right]$$

Linearized Folded Gilbert cell $I_{OUT} = \sqrt{2k_n k_p} V_x V_y$



Sum of squares multiplier



Assume $\left(\frac{W}{L}\right)_2 \gg \left(\frac{W}{L}\right)_1$

$\Rightarrow M_2$ is a source follower

$\Rightarrow I_{D1} = k_1(V_{G1} - V_{S1} - V_T)^2 = k_1(V_{G1} - V_{G2} - V_T)^2$ (stronger than M_1)

$$I_1 = k(V_1 - V_2 - V_T)^2$$

$$I_2 = k(V_1 + V_2 - V_T)^2$$

$$I_3 = k(-V_1 + V_2 - V_T)^2$$

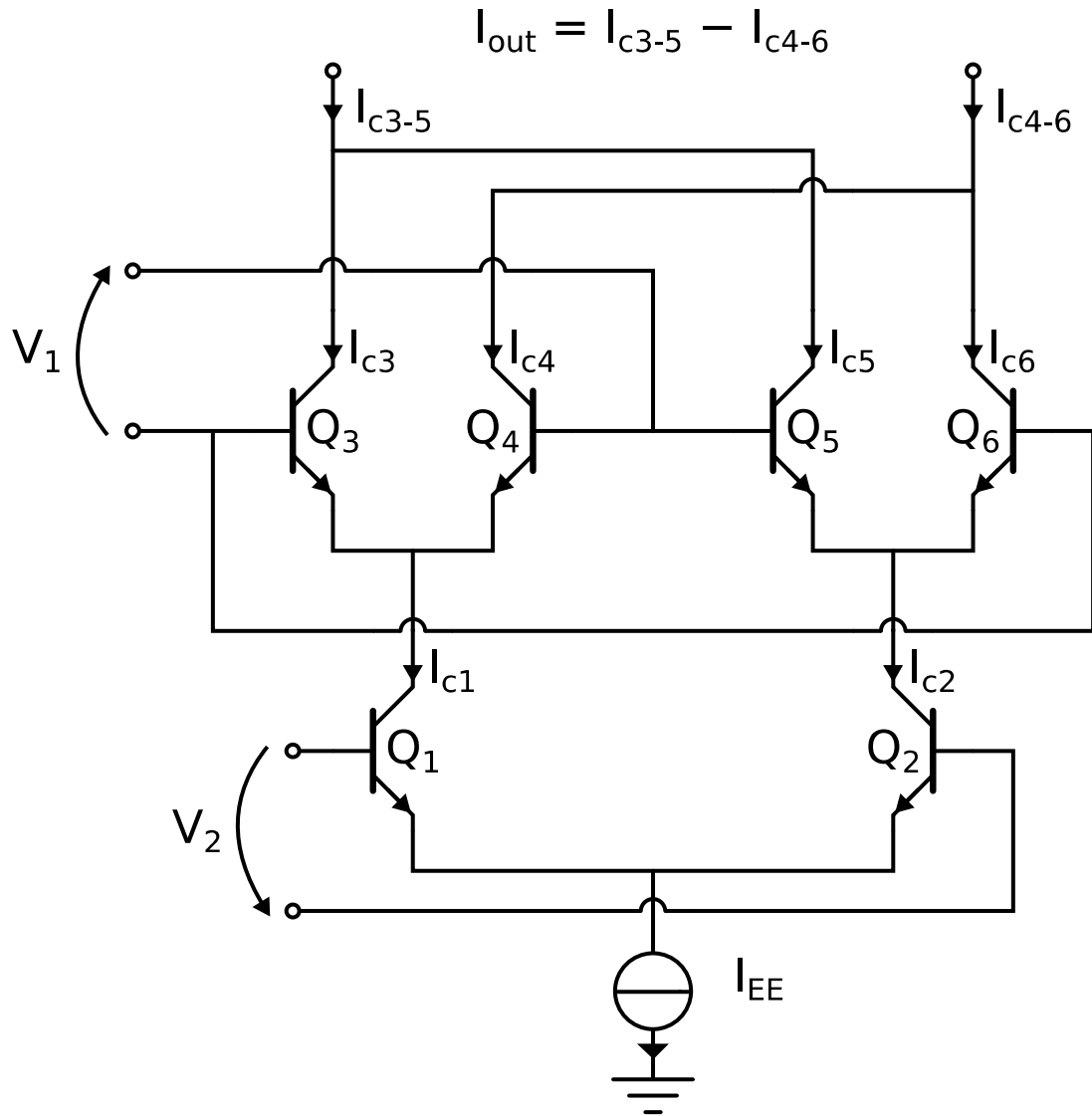
$$I_4 = k(-V_1 - V_2 - V_T)^2$$

$$\begin{aligned} I_1 - I_2 &= k(V_1 - V_T)^2 - 2k(V_1 - V_T) \cdot V_2 + kV_2^2 \\ &\quad - k(V_1 - V_T)^2 - 2k(V_1 - V_T)V_2 + kV_2^2 - kV_2^2 \\ &= -4k(V_1 - V_T)V_2 = -4kV_1V_2 + 4kV_TV_2 \end{aligned}$$

$$-(I_3 - I_4) = 4k(-V_1 - V_T) \cdot (-V_2) = 4kV_1V_2 + 4kV_TV_2$$

$$I_1 - I_2 - (-(I_3 - I_4)) = -8kV_1V_2$$

Gilbert multiplier



Current equations:

$$I_{OUT} = (I_{c3} + I_{c5}) - (I_{c4} + I_{c6})$$

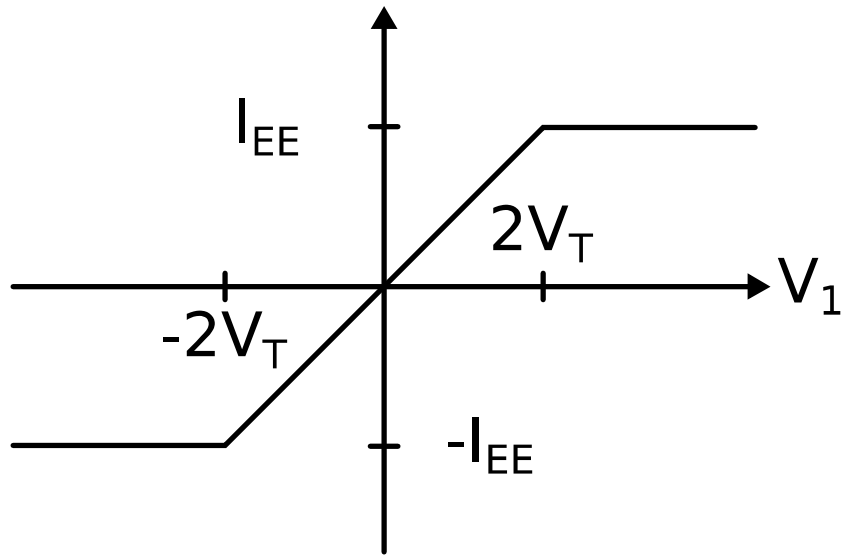
$$= (I_{c3} - I_{c6}) - (I_{c4} - I_{c5})$$

Emitter coupled pair:

$$I_{c3} = \frac{I_{c1}}{1 + \exp\left(\frac{-V_1}{V_T}\right)} \quad I_{c4} = \frac{I_{c1}}{1 + \exp\left(\frac{V_1}{V_T}\right)}$$

$$I_{c5} = \frac{I_{c2}}{1 + \exp\left(\frac{V_1}{V_T}\right)} \quad I_{c6} = \frac{I_{c2}}{1 + \exp\left(\frac{-V_1}{V_T}\right)}$$

$$I_{c1} = \frac{I_{EE}}{1 + \exp\left(\frac{-V_2}{V_T}\right)} \quad I_{c2} = \frac{I_{EE}}{1 + \exp\left(\frac{V_2}{V_T}\right)}$$



$$I_{C3} = \frac{I_{EE}}{\left[1 + \exp\left(\frac{-V_1}{V_T}\right)\right] \left[1 + \exp\left(\frac{-V_2}{V_T}\right)\right]}$$

$$I_{C4} = \frac{I_{EE}}{\left[1 + \exp\left(\frac{-V_2}{V_T}\right)\right] \left[1 + \exp\left(\frac{V_1}{V_T}\right)\right]}$$

$$I_{C5} = \frac{I_{EE}}{\left[1 + \exp\left(\frac{V_1}{V_T}\right)\right] \left[1 + \exp\left(\frac{V_2}{V_T}\right)\right]}$$

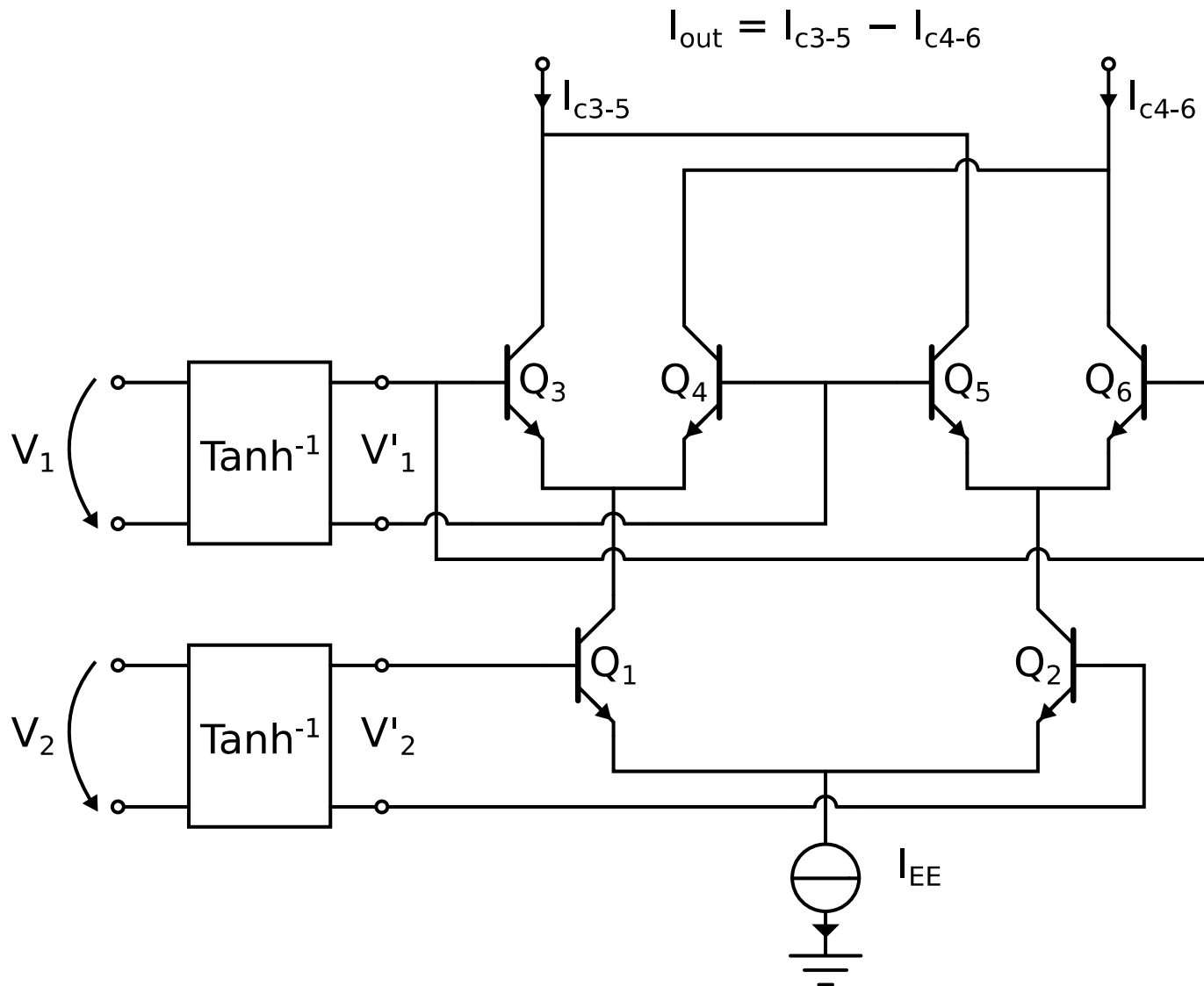
$$I_{C6} = \frac{I_{EE}}{\left[1 + \exp\left(\frac{V_2}{V_T}\right)\right] \left[1 + \exp\left(\frac{-V_1}{V_T}\right)\right]}$$

$$\Rightarrow I_{OUT} = I_{EE} \left[\tanh\left(\frac{V_1}{2V_T}\right) \right] \left[\tanh\left(\frac{V_2}{2V_T}\right) \right]$$

when $V_1 \ll 2V_T$ and $V_2 \ll 2V_T$ $\Rightarrow \tanh\frac{V_1}{2V_T} \approx \frac{V_1}{2V_T}$

$$\Rightarrow I_{OUT} \approx I_{EE} \left(\frac{V_1}{2V_T}\right) \left(\frac{V_2}{2V_T}\right)$$

Linearized Gilbert multiplier



Linearized Gilbert multiplier

Voltage to current converter:

$$I_1 = I_{01} + k_1 V_1$$

$$I_2 = I_{01} - k_1 V_1$$

Output voltage:

$$\Delta V = V_{BE1} - V_{BE2}$$

$$= V_T \ln \left(\frac{I_{01} + k_1 V_1}{I_s} \right) - V_T \ln \left(\frac{I_{01} - k_1 V_1}{I_s} \right)$$

$$= V_T \ln \left(\frac{I_{01} + k_1 V_1}{I_{01} - k_1 V_1} \right)$$

$$\Rightarrow \Delta V = 2V_T \tanh^{-1} \left(\frac{k_1 V_1}{I_{01}} \right) ; \tanh^{-1}(x) = \frac{1}{2} \ln \left(\frac{1+x}{1-x} \right)$$

When applied for Gilbert multiplier input \tanh^{-1} compensates for \tanh function

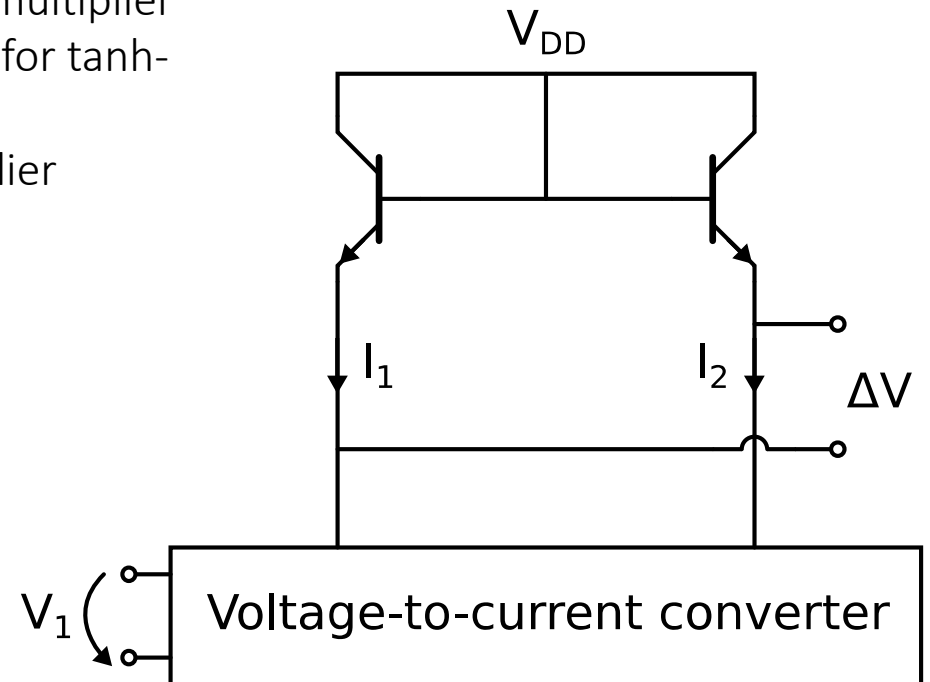
\Rightarrow We obtain ideal multiplier

$$\Rightarrow \Delta I = I_{EE} \left(\frac{k_1 V_1}{I_{01}} \right) \left(\frac{k_2 V_2}{I_{02}} \right)$$

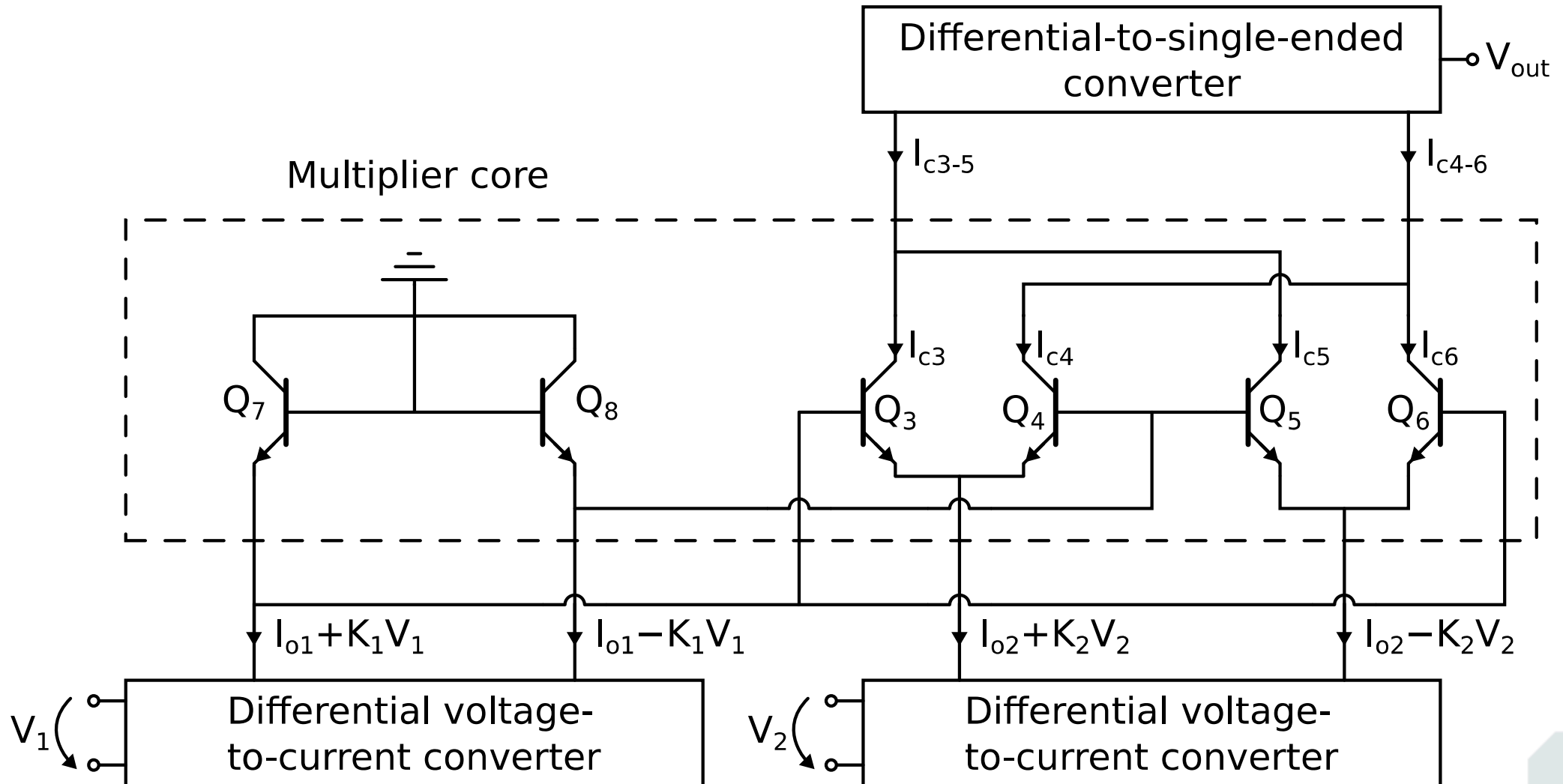
Linear region:

$$-\frac{I_{01}}{k_1} < V_1 < \frac{I_{01}}{k_1}$$

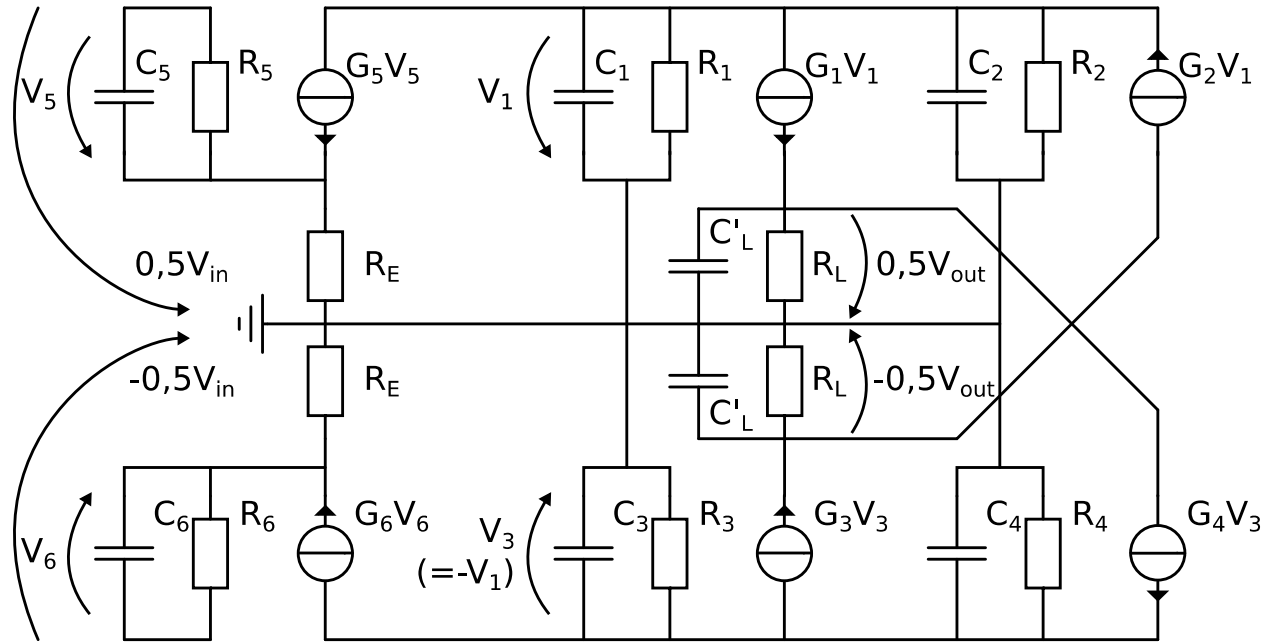
$$-\frac{I_{02}}{k_2} < V_2 < \frac{I_{02}}{k_2}$$



Linearized Gilbert multiplier



The small signal equivalent circuit of the Gilbert multiplier:



The multiplier has three poles:

- The pole associated with the base node of the input transistors:

$$p_1 = -\frac{R_{b5} + R_5 + R_{E1}}{R_5(R_{b5} + R_{E1})C_5} \approx -\frac{1}{R_E g_{m5}} \cdot \omega_{Tmax}$$

- The pole associated with the load:

$$p_2 = -\frac{1}{R_L(C_R + C_\mu^1 + C_\mu^3 + C_{js}^1 + C_{js}^3 + C_L)} = -\frac{1}{R_L C_L}$$

- The pole associated with the control transistors (cascode):

$$p_3 = -\frac{g_1 + g_2}{C_1 + C_2} \approx -\omega_T$$

CMOS Bridge-type multiplier

Assume transistors $M_{1...4}$ in linear region.

$$I_{DS} = \beta_1 (V_{GS} - V_T - \frac{1}{2} V_{DS}) V_{DS}$$

Current equations of transistors $M_{1...4}$:

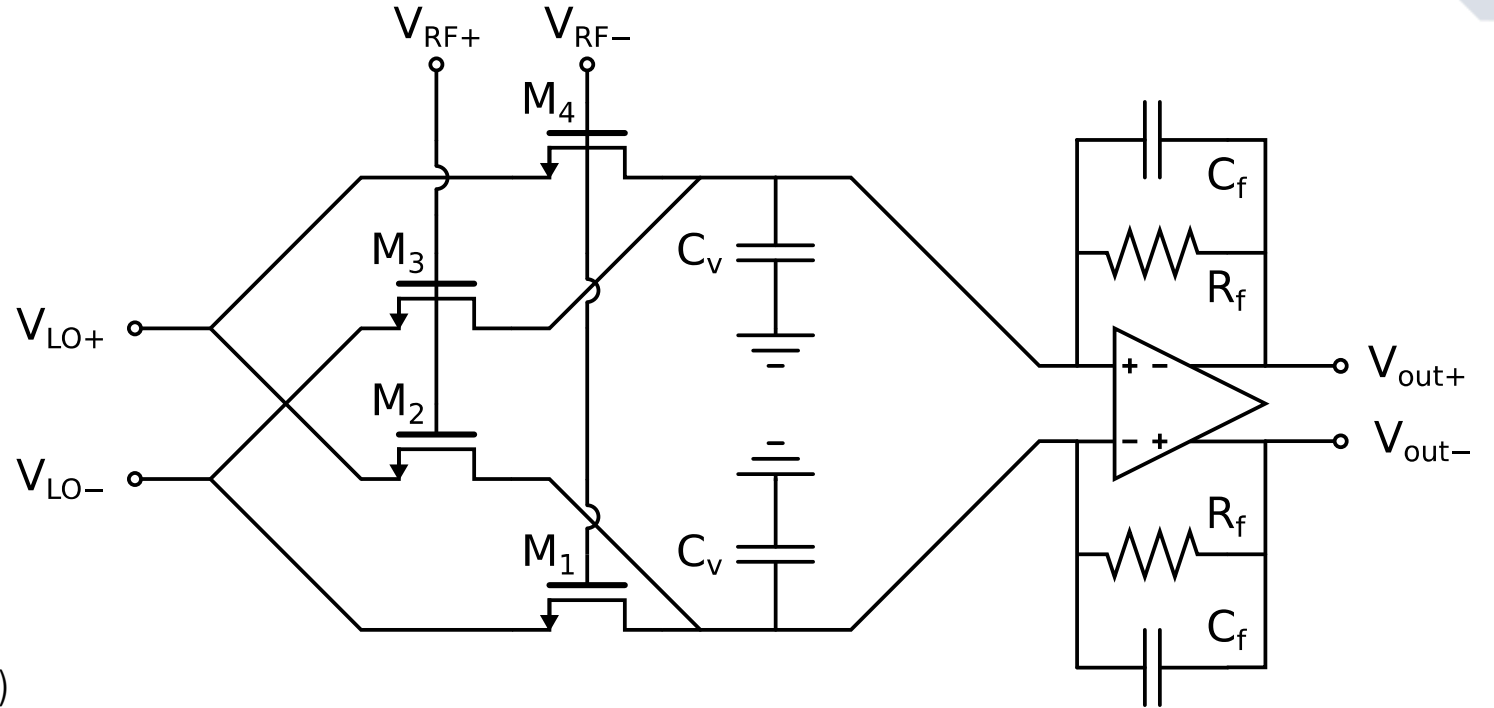
$$I_{DS,1} = \beta_1 (V_{RF}^+ - V_{CM} - V_{Tn,1} - \frac{V_{LO}^+ - V_{CM}}{2}) \cdot (V_{LO}^+ - V_{CM})$$

$$I_{DS,2} = \beta_2 (V_{RF}^- - V_{CM} - V_{Tn,2} - \frac{V_{LO}^- - V_{CM}}{2}) \cdot (V_{LO}^- - V_{CM})$$

$$I_{DS,3} = \beta_3 (V_{RF}^+ - V_{CM} - V_{Tn,3} - \frac{V_{LO}^- - V_{CM}}{2}) \cdot (V_{LO}^- - V_{CM})$$

$$I_{DS,4} = \beta_4 (V_{RF}^- - V_{CM} - V_{Tn,4} - \frac{V_{LO}^+ - V_{CM}}{2}) \cdot (V_{LO}^+ - V_{CM})$$

V_{RF} and V_{LO} have the same common-mode level V_{CM} .

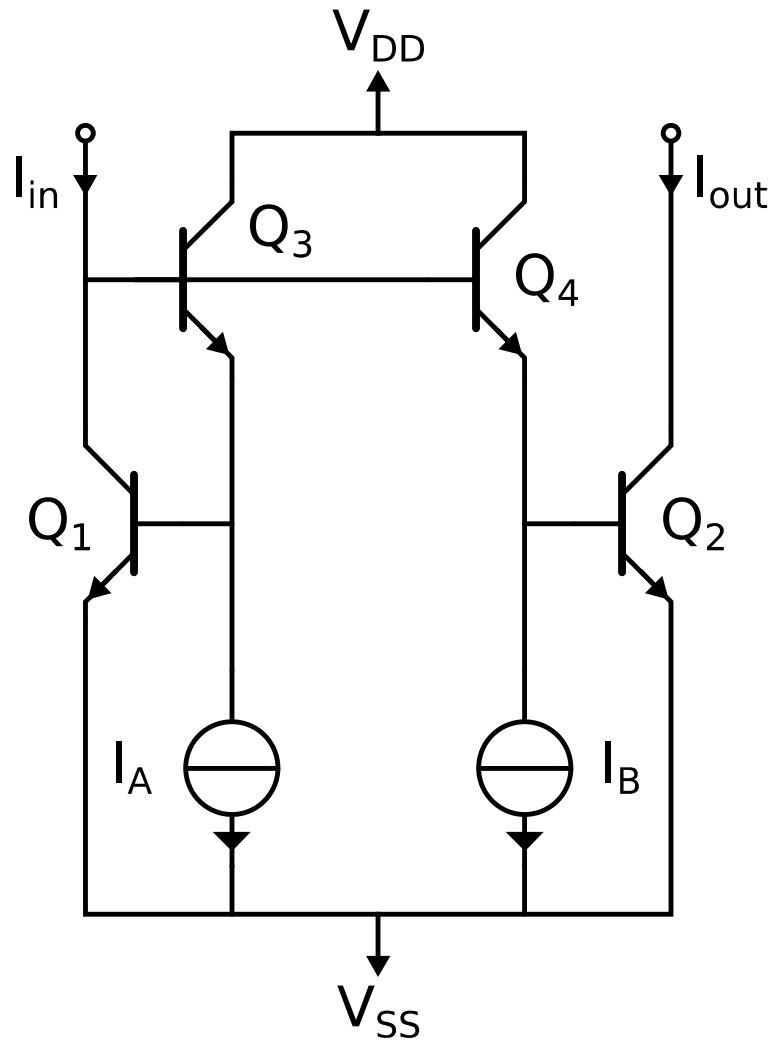


Differential output voltage:

$$\begin{aligned} V_{OUT}^+ - V_{OUT}^- &= -(R_f(I_1 + I_2) - R_f(I_3 + I_4)) \\ &= R_f((I_1 - I_4) - (I_3 - I_2)) \\ &= \beta * R_f (V_{RF}^+ - V_{RF}^-) \cdot (V_{LO}^+ - V_{LO}^-) \end{aligned}$$

Ideal multiplier!

The current multiplier (B. Gilbert)



Voltage equation:

$$V_{BE1} + V_{BE3} = V_{BE2} + V_{BE4}$$

Applying the collector current equation of BJT:

$$V_T \ln\left(\frac{I_{C1}}{I_S}\right) + V_T \ln\left(\frac{I_{C3}}{I_S}\right) = V_T \ln\left(\frac{I_{C2}}{I_S}\right) + V_T \ln\left(\frac{I_{C4}}{I_S}\right)$$

$$\Rightarrow \ln\left(\frac{I_{C1}}{I_B}\right) = \ln\left(\frac{I_{C2}}{I_A}\right)$$

Assuming $I_{in} = I_{C1}$ and $I_{out} = I_{C2}$

we obtain $I_{out} = \frac{I_A}{I_B} I_{in}$