### **ELEC-E8417 Switched-Mode Power Supplies**

Exam 2.6.2022

# ONLINE EXAM

### **Question 1**

In a Buck converter output voltage  $U_0 = 5$  V, supply voltage  $U_d = 48$  V, output power  $P_0 = 5$  W, and switching frequency  $f_s = 50$  kHz. Output filter components (L and C) are so large that output current of the converter is ideal dc. Harmonic currents of the supply voltage source are limited, and the switching frequency component can in maximum be 0,3 mA. What is the maximum allowed resonance frequency of the input filter?

### **Question 2**

In a Forward converter output voltage  $U_0 = 5$  V, supply voltage  $U_d = 48$  V ± 15 % V, output power 20 W  $\leq P_0 \leq 55$  W, switching frequency  $f_s = 100$  kHz, and  $N_1:N_3 = 1$ . Calculate the minimum value of ratio  $N_2:N_1$  that can be used. Calculate the minimum value of the output filter inductance so that output filter remains in continuous conduction mode.

### **Question 3**

In a Forward converter output voltage  $U_0 = 12$  V, supply voltage 200 V  $\leq U_d \leq 320$  V, output power 15 W  $\leq P_0 \leq 50$  W and switching frequency  $f_s = 50$  kHz. The transformer is built from ferrite 3C8 with ETD 34 core. Its volume is 7640 mm<sup>3</sup>, effective area 97,1 mm<sup>2</sup> and the smallest surface area of the core is A<sub>c,min</sub> = 86,6 mm<sup>2</sup>. Copper filling factor is assumed to be 0,6 and saturation flux density at 100 °C is 0,32 T. Calculate the number of turns in the transformer when demagnetization winding  $N_3 = N_1$ . Remanence flux density can be assumed to be zero. Primary inductance of the transformer is 10 mH, calculate the needed airgap length.

### Question 4

Efficiency is important in switched mode power supplies. One part of the losses are the switching losses of the power semiconductor devices. What kind of methods can be used to reduce the losses created by these switches in SMPS. Describe also shortly other sources of losses in SMPS.

# **Question 5**

What kind of methods can be used to implement current-mode control in SMPS? What are the advantages of current-mode control when compared to the voltage-mode control (PWM duty ratio control)?



UD= DU1 => D= 782 9109

Current from the suppy depends



ac-analyse



For currents  $i_{1}(s) = i_{5}(s) + i_{1}(s) = i_{5}(s) - i_{1}(s)s^{2}L_{1}(s)$  $= \frac{l_{1}(s)}{l_{2}(s)} = \frac{1}{1+s^{2}L_{1}c_{1}} = \frac{1}{1-(\frac{1}{2}u_{0})^{2}}$  $w_{0i} = J_2 c_i^7$ Saitching Joequenez component of is hauld be less than 0,3 mA => with

# should be less than 0,3 mA => with absolute values



Queston 2

It has been shown in fecture that in Forward  $V_0 = \frac{N_z}{N_1} D V_1$ 

We should be able to get the desired output Us = 5V with the varying input V = 48± 15% = 40,80V 2 V 2 55,200

Pernagnetizing the core requires des Pmas =0,5 Therefore we need to calculate with Ymm

(N2) - Vo N, mm = Pmar Jamin = 0,5, 40,80 = 4,08 2 4

Ouring switch off, i.e. (1-D) The output inductor current is decreases from the peak value I to zero.

 $i_1 = I_1 - \frac{1}{2}(1-p)I_5 = 0$  as we are in the

borderliner of CCM and RCM. Average value of 12 is equal to Ig and thus

Is = = I = = 2L (1-0) Ts and then

Lmin = 1- Pmin Vo Ts 2 2 IzB, min Vo Ts 2 4 ut

Questro 0 3

# In Porvaré - converter det ration 13 United due to the Eurognotisation time needed



In Forward magnetisation is only in one quadrant and therefore Bsad

A Bmax 2 2

Here input voltage can change and saturation should not occur oven with the nightst voltage  $\frac{B_{sat}}{2} \cdot \frac{V_{sunn}}{V_{sunax}} = \frac{0,32}{2} \cdot \frac{200}{320^2}$ = 2 · Vanak



